Transport through small metallic islands, defined in semiconductor heterostructures, has been of considerable interest recently.\textsuperscript{1} When the island is only weakly coupled to reservoirs via tunnel barriers, the Coulomb blockade (CB) effect determines the carrier transport. In such islands, the interplay between interactions and size quantization can be studied. A large number of experiments have investigated the properties of single electron transistors made out of conventional metals, and of high mobility, low density two-dimensional electron gases, defined in mostly Ga\textsubscript{Al}As or Si, among other materials.\textsuperscript{1} Single hole transistors out of \textit{p}-Si/\textit{SiGe} quantum wells are of particular interest for several reasons: (i) the material has a large \textit{g} factor of \textit{g}_{\textit{eff}}=6.7 and a large effective mass (\textit{m}_{\textit{e}}=0.25\textit{m}_{\textit{e}}), (ii) the interaction parameter \textit{r}_{\textit{S}}, defined as the ratio between the Coulomb energy of two holes at their average separation, and the Fermi energy, is very large, i.e., \textit{r}_{\textit{S}}=4 for typical carrier densities, and (iii) the properties of the quantum well are strongly influenced by biaxial strain, due to the lattice mismatch at the Si/\textit{SiGe} interface.

However, \textit{p}-Si\textit{Ge} has been notoriously difficult to gate, due to leakage current currents across the Schottky barrier.\textsuperscript{2,3} Recently, progress has been reported in this respect. Emeleus \textit{et al.}\textsuperscript{4} have tuned the hole density in a \textit{p}-Si/\textit{SiGe} quantum well using a homogeneous backgate. Homogeneous top gating of this material has been reported as well.\textsuperscript{5} In those samples, the two-dimensional hole gas (2D\textit{HG}) is buried very deeply below the surface, which limits the pattern transfer into the 2D\textit{HG}. The fabrication of any kind of gated nanostructure in \textit{p}-Si/\textit{SiGe} has not been reported yet. It should be noted that small islands and wires have been patterned out of \textit{p}-Si\textsubscript{1-x}Ge\textsubscript{x}/Si resonant tunneling structures by etching.\textsuperscript{6} Furthermore, single hole charging effects have been observed in \textit{p}-Si\textsuperscript{7} and in two-terminal, \textit{\delta}-doped Si\textit{Ge} devices.\textsuperscript{8}

Here, we present the fabrication and characterization of a gated single hole transistor in a \textit{p}-Si/\textit{SiGe} quantum well. The gate leakage is reduced by oxidizing the etched surface prior to patterning of the top gates. At low temperatures, we observe clear single hole tunneling and CB oscillations.

The samples have been grown by molecular beam epitaxy, and contain a 200 ÅSi\textsubscript{0.85}Ge\textsubscript{0.15} layer on top of an undoped Si substrate, a 150 Å B-doped Si layer with a setback of 180 Å from the well, and a 200 Å undoped Si cap. The Si\textsubscript{0.85}Ge\textsubscript{0.15} layer forms a triangular potential well for the two-dimensional hole gas [Fig. 1(a)]. Due to the lattice mismatch between Si and Si\textsubscript{0.85}Ge\textsubscript{0.15}, the heavy hole (\textit{m}_{\textit{J}}=\pm 3/2) potential is split from the light hole (\textit{m}_{\textit{J}}=\pm 1/2) potential, and ensures that the lowest occupied bound state has heavy hole character. The hole density of the ungated 2D\textit{HG} is \textit{p}_{\textit{2D}}=3.6\times10^{15}\text{m}^{-2}, and an elastic mean free path of \textit{\ell}_{\textit{e}}=100\text{nm} is obtained from magnetoresistivity measurements.

Tests have indicated that in our samples, large leakage currents between the top gate and the 2D\textit{HG} can flow across the mesa edge. Therefore, we have deposited a layer of Si\textit{O}_{2} by thermal evaporation right after the reactive ion etch that defines the mesa, i.e., with the photoresist still on the mesa, Fig. 1(b). We have used this technique in earlier work for the fabrication of homogeneous gates.\textsuperscript{9} After lift-off of the oxide, the top gate (10 nm Ti and 20 nm Al) was patterned by electron beam lithography and thermal evaporation. This sequence of technology steps reduces leakage currents in our samples significantly.

The geometry of the gates is crucial. For grounded Ti/Al gates in the submicron regime, the 2D\textit{HG} is depleted under-
neath, with a lateral depletion length of roughly 100 nm. This depletion is probably caused by strain and pinning on defects, which complicates a simulation of the device. Preliminary experimental studies on split gates with various openings \( w \) between the two electrodes were used to develop the gate geometry. For \( w < 100 \text{ nm} \), we were unable to open the channel between the split gates without simultaneously populating the regions below the gates. For \( w > 250 \text{ nm} \), the channels could not be pinched off. Also, the pinch-off voltage of split gates with identical geometry can be quite different.

In Fig. 1(c), the island geometry is shown. Three split gate electrodes (point contact gates \( \text{pc1}, \text{pc2} \), as well as the center gates \( \text{cg} \)) define the dot with a lithographic size of \( 500 \text{ nm} \times 800 \text{ nm} \). For \( \text{pc1} \) and \( \text{pc2} \), \( w = 150 \text{ nm} \) was chosen.

The measurements have been carried out in a \(^3\text{He}/^4\text{He} \) dilution refrigerator with a base temperature of 90 mK. dc voltages \( V_{sd} \) are applied between source and drain, and the current is measured with a resolution of 500 fA.

We have studied two different quantum dots of identical geometry and performed several cooldowns, all leading to similar results. Here, we focus on measurements from one device, taken in a single cooldown.

Split gates \( \text{pc1} \) and \( \text{pc2} \) are used to adjust the coupling of the island to source and drain [Fig. 2(a)]. Due to the small elastic mean free path, conductance quantization is not observed. The leakage currents measured at the center gates is shown in the inset of Fig. 2(a). For temperatures between 0.1 and 2 K investigated here, neither the resistance of the split gates, nor the leakage currents revealed any temperature dependence within experimental accuracy.

At sufficiently large split gate voltages \( V_{\text{pc1}} \) and \( V_{\text{pc2}} \) (the working points are chosen to be \( V_{\text{pc1}} = -140 \text{ mV} \) and \( V_{\text{pc2}} = 180 \text{ mV} \); the corresponding leakage currents are 0.5 and 1 pA, respectively), CB oscillations are observed as a function of the center gate voltage \( V_{\text{cg}} \) [Fig. 2(b)]. Here, \( V_{sd} \) was 100 \text{ mV}. The measured CB resonances can be fitted well to the expression describing a thermally smeared CB peak in the multilevel transport regime, i.e.,

\[
G(V_{\text{cg}}, T_h) = G_{\text{max}} \cos \left( \frac{\eta (V_{\text{cg}} - V_{\text{max}}) / 2.5 k_B T_h}{V_{\text{sd}}} \right). \]

Here, \( G_{\text{max}} \) and \( V_{\text{max}} \) denote the amplitude and the position of the CB resonance, respectively. The lever arm \( \eta = eE / dV_{\text{cg}} \) (\( dV_{\text{cg}} \) is the Coulomb blockade oscillation period and \( E = eV_{sd} \) is the energy inside the island) translates \( V_{cg} \) into energy and is obtained from measurements of the CB diamonds [Fig. 3(a)].

We find \( \eta = 0.68 \text{ eV/V} \). As fit parameter, we obtain hole temperatures of \( T_h = 390 - 570 \text{ mK} \), depending on the peak. A Lorentzian, on the other hand, which is appropriate for homogeneously broadened resonances, fits significantly worse [inset in Fig. 2(b)]. The full widths at half maximum (FWHMs) thus fluctuate strongly from peak to peak, and furthermore saturate for bath temperatures below 400 mK, as well as for source-drain bias voltages below \( V_{sd} = 120 \text{ mV} \). We conclude that neither the electron temperature, nor \( V_{sd} \) determine the FWHM in this regime (\( T < 400 \text{ mK} \) and \( V_{sd} < 120 \text{ mV} \)). Furthermore, we observe a pronounced pair modulation of the peak separations in this regime.
ively similar modulations are observed in both devices and in all cooldowns, although the detailed structure varies strongly. A measurement of the Coulomb diamonds, i.e., the current as a function of both \( V_{sd} \) and \( V_{cg} \) is shown in Fig. 3(a). The stability of the diamonds shows that there is little telegraph noise and switching at frequencies below 1 Hz, the bandwidth of our setup. However, noise of higher frequencies may be substantial; gate voltage dependent charge rearrangements in the substrate may be present and could determine the FWHM of the CB peaks. Note also that the leakage currents are not negligible as compared to the current through the dot and may modify the line shape as well.

In Fig. 3(b), we show the temperature dependence of the CB oscillations. As the temperature increases, not only the CB oscillations are smeared out, but also a striking overall increase in \( G \) is observed, which indicates thermally enhanced transport through the island. Since neither the conductance of the individual tunnel barriers, measured with positive voltages applied to the remaining gates, nor the leakage currents depend on temperature between 90 mK and 2 K, the observed overall increase of \( G \) is entirely due to the island. Several scenarios could lead to such a behavior, such as variable-range hopping\(^{10}\) or Efros–Shklovskii hopping.\(^{11}\) Reasonable agreement with variable-range hopping (i.e., \( \rho \approx \exp \left( \left( T_0 / T \right)^{1/3} \right) \)) is found, and we can estimate the characteristic temperature from fits to \( T_0 \approx 0.55 \) K. This result depends only weakly on \( V_{cg} \). We cannot, however, distinguish between variable-range hopping and other possible hopping mechanisms, due to the limited range of temperatures.

Similar modulations of Coulomb diamonds have been observed in double dot systems for weak coupling between the dots.\(^{12}\) Possibly, the modulation is thus an indication that at \( V_{cg} \approx 40 \) mV, the island splits up into two smaller, weakly coupled puddles, due to disorder. The modulated diamonds then reflect the formation of a “molecular state,” or of a capacitance between the two puddles. Measurements on arrays of two puddles in parallel\(^{13}\) and of two as well as of three puddles in series\(^{14}\) have been reported. From those experiments, it is clear that more puddles than two would result in larger modulation periods. Since we cannot tune the two puddles independently nor the coupling between them, we are unable to extract the puddle sizes, the interdot conductance, or their configuration with respect to source and drain. Nevertheless, within this interpretation, the average CB period in Fig. 3(a) corresponds to \( e/C_g \), where \( e \) is the elementary charge and \( C_g \) the capacitance between the total island and the center gates. The average distance between the diamond corners along the \( V_{cg} \) direction corresponds to twice the charging energy \( E_C = e^2 / C_g \) of the island: \( \delta V_{sd} = 2e / C_g \). Here, \( C_g \) denotes the total capacitance of the island. From Fig. 3(a), we find \( C_g \approx 170 \) aF, which corresponds to a single electronic island radius of \( r \approx 200 \) nm. Hence, the level spacing inside the dot can be estimated as \( \Delta \approx 8 \) µeV, which justifies our assumption of multilevel transport used earlier.

In summary, we have demonstrated that top gate defined nanostructures can be fabricated and operated on p-SiGe. The leakage currents can be significantly reduced by oxidizing the mesa edges before top gate deposition. We have observed Coulomb blockade on a top gate defined island in p-Si/SiGe. The island is strongly disordered, which manifests itself in thermally enhanced transport as well as in a possible puddle formation at small hole densities.

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